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Attorney Docket No. 5646-42DVIP

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re: Au et al.

Confirmation No.: 6889

Serial No.: 10/612,849

Group Art Unit: 2186

JUN 02 2005

Filed: July 3, 2003

For: FIFO MEMORY DEVICES HAVING MULTI-PORT CACHE MEMORY ARRAYS
THEREIN THAT SUPPORT HIDDEN EDC LATENCY AND BUS-MATCHING
AND METHODS OF OPERATING SAME

June 2, 2005

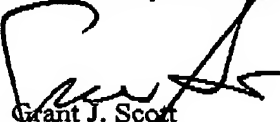
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRY

Sir:

Applicants respectfully inquire as to the status of the above-identified patent application, and when the next communication from the U.S. Patent and Trademark Office regarding this application can be expected.

Respectfully submitted,

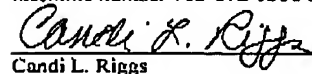


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Candi L. Riggs